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Docket Number (Optional)

FIS920030349US1

Application Number

10/708,788

Applicant(s)

JOSEPH O. MARSH, ET AL.

Filing Date

3/25/04

Group Art Unit

~~NOT ASSIGNED~~ 2829

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

/RV/	"DIGITAL SYSTEMS TESTING AND TESTABLE DESIGN", IEEE PRESS, Ch. 9.3 - 9.9, 1990.
/RV/	"BIST FOR D/A AND A/D CONVERTERS", IEEE Design & Test of Computers v. 13 n. 4 Winter 1996, pp. 40 - 49

EXAMINER

/Roberto Velez/

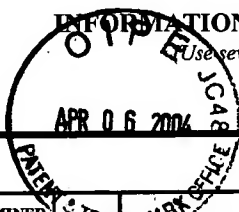
DATE CONSIDERED

/Roberto Velez/

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INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)



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*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING IF APPRO

FOREIGN PATENT DOCUMENTS

REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

/RV/	Serial No. 10/604,025, "DATA TRANSCEIVER AND METHOD FOR EQUALIZING THE DATA EYE OF A DIFFERENTIAL INPUT DATA SIGNAL ", Camara, et al.

EXAMINER

/Roberto Velez/

DATE CONSIDERED

04/21/2008

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